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TRANSMITTAL FORM

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Total Number of Pages in This Submission

Application Number 10/660,016

Filing Date September 11, 2003

First Named Inventor Andrew G. Jenkins

Art Unit 2825

Examiner Name Vuthe Siek

Attorney Docket Number X-1418 US

ENCLOSURES (Check all that apply)

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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name XILINX, INC. CUSTOMER NO: 24309

Signature

Printed name Kim Kanzaki

Date June 6, 2005

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X-1418 US
10/660,016

PATENT
Conf. No.: 5910



IN THE UNITED STATES PATENT OFFICE

Applicant: Andrew G. Jenkins
Assignee: Xilinx, Inc.
Title: Antenna Violation Correction In High-Density Integrated Circuits
Serial No.: 10/660,016 File Date: 09-11-03
Examiner: Vuthe Siek Art Unit: 2825
Docket No.: X-1418 US Conf. No.: 5910

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RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action mailed from the Patent Office on March 8, 2005, please substitute the following claims as indicated.

Listing of the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 11 of this paper.